**D PACKAGE** 

(TOP VIEW)

NC

CATHODE

- Low Temperature Coefficient
- Wide Operating Current . . . 400 μA to 10 mA
- 0.27-Ω Dynamic Impedance
- ±1% Tolerance Available
- Specified Temperature Stability
- Easily Trimmed for Minimum Temperature Drift
- Fast Turnon
- Three-Lead Transistor Package

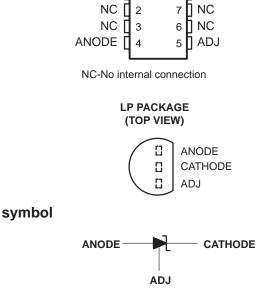
## description

The LM236-2.5, LM336-2.5, and LM336B-2.5 integrated circuits are precision 2.5-V shunt regulator diodes. These reference circuits operate as low-temperature-coefficient 2.5-V zeners with a 0.2- $\Omega$  dynamic impedance. A third terminal provided on the circuit allows the reference voltage and temperature coefficient to be trimmed easily.

The series is useful as precision 2.5-V low-voltage references (V<sub>7</sub>) for digital voltmeters, power

supplies, or operational-amplifier circuitry. The 2.5-V voltage reference makes it convenient to obtain a stable reference from 5-V logic supplies. Devices in this series operate as shunt regulators, and can be used as either positive or negative voltage references.

The LM236-2.5 is characterized for operation from  $-25^{\circ}$ C to  $85^{\circ}$ C. The LM336-2.5 and LM336B-2.5 are characterized for operation from  $0^{\circ}$ C to  $70^{\circ}$ C.



#### **AVAILABLE OPTIONS**

	PACKAGED DEVICES						
TA	SMALL OUTLINE (D)	PLASTIC (LP)					
0°C to 70°C	LM336D-2.5	LM336LP-2.5					
0.0 10 70.0	LM336BD-2.5	LM336BLP-2.5					
–25°C to 85°C	LM236D-2.5	LM236LP-2.5					

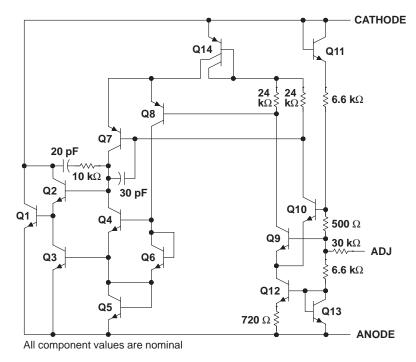
The D and LP packages also are available taped and reeled. Add the suffix R to the device type (e.g., LM336DR-2.5). Chip forms are tested at  $25^{\circ}$ C.



Please be aware that an important notice concerning availability, standard warranty, and use in critical applications of Texas Instruments semiconductor products and disclaimers thereto appears at the end of this data sheet.



# schematic diagram



# absolute maximum ratings over operating free-air temperature range (unless otherwise noted)†

Reverse current, I <sub>R</sub>	20 mA
Forward current, I <sub>F</sub>	10 mA
Package thermal impedance, θ <sub>JA</sub> (see Notes 1 and 2): D package	. 97°C/W
LP package	156°C/W
Lead temperature 1,6 mm (1/16 inch) from case for 10 seconds: D or LP package	260°C
Storage temperature range, T <sub>stg</sub> 65°C	to 150°C

<sup>†</sup> Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

# recommended operating conditions

		MIN	MAX	UNIT
Operating free cir temperature T.	LM236-2.5	-25	85	°C
Operating free-air temperature, T <sub>A</sub>	LM336-2.5, LM336B-2.5	0	70	



NOTES: 1. Maximum power dissipation is a function of  $T_J(max)$ ,  $\theta_{JA}$ , and  $T_A$ . The maximum allowable power dissipation at any allowable ambient temperature is  $P_D = (T_J(max) - T_A)/\theta_{JA}$ . Operating at the absolute maximum  $T_J$  of 150°C can impact reliability.

<sup>2.</sup> The package thermal impedance is calculated in accordance with JESD 51-7.

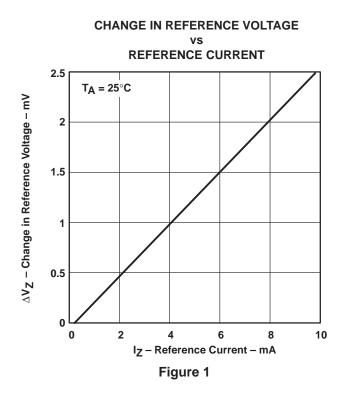
SLVS063C - NOVEMBER 1988 - REVISED AUGUST 2000

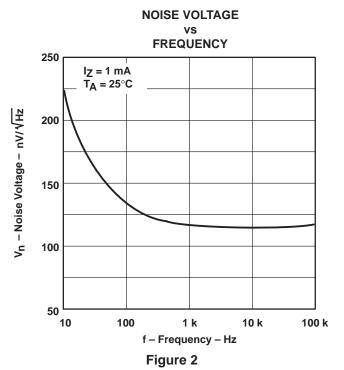
# electrical characteristics at specified free-air temperature (unless otherwise noted)

PARAMETER		TEST CONDITIONS	T <sub>A</sub> †	LM236-2.5		LM336-2.5			UNIT		
		1EST CONDITIONS		MIN	TYP	MAX	MIN	TYP	MAX	UNII	
VZ	Reference voltage	I <sub>Z</sub> = 1 mA	LM236, LM336	25°C	2.44	2.49	2.54	2.39	2.49	2.59	V
			LM336B				2.44	2.49	2.54	V	
$\Delta V_{Z(\Delta T)}$	Change in reference voltage with temperature	V <sub>Z</sub> adjusted to 2.490 V, I <sub>Z</sub> = 1 mA		Full range		3.5	9		1.8	6	mV
Change in reference	I= - 400 :: A to 10 mA	25°C		2.6	6		2.6	10	mV		
$\Delta VZ(\Delta I)$	voltage with current	$I_Z = 400 \mu\text{A} \text{ to } 10 \text{mA}$	Full range		3	10		3	12	IIIV	
$\Delta V_{Z(\Delta t)}$	Long-term change in reference voltage	I <sub>Z</sub> = 1 mA		25°C		20			20		ppm/khr
z <sub>Z</sub>	Reference impedance	II = 1  m/s  f = 1  kHz	25°C		0.2	0.6		0.2	1	w	
			Full range		0.4	1		0.4	1.4	VV	

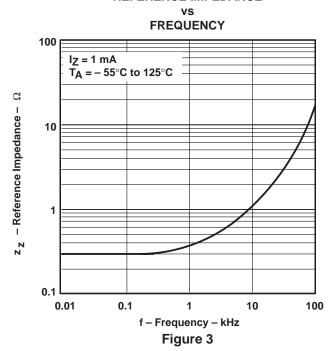
<sup>&</sup>lt;sup>†</sup> Full range is –25°C to 85°C for the LM236-2.5 and 0°C to 70°C for the LM336-2.5 and LM336B-2.5.

## TYPICAL CHARACTERISTICS





### REFERENCE IMPEDANCE





### **APPLICATION INFORMATION**

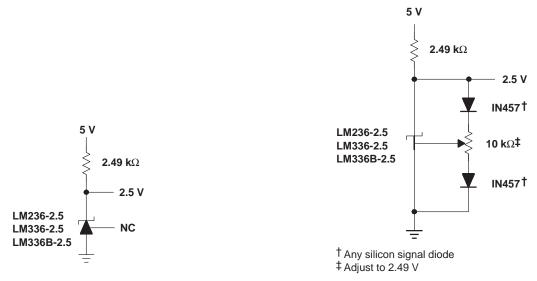


Figure 4. 2.5-V Reference

Figure 5. 2.5-V Reference With Minimum Temperature Coefficient

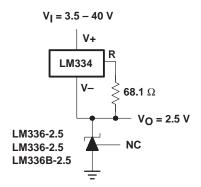


Figure 6. Wide-Input-Range Reference

#### **IMPORTANT NOTICE**

Texas Instruments and its subsidiaries (TI) reserve the right to make changes to their products or to discontinue any product or service without notice, and advise customers to obtain the latest version of relevant information to verify, before placing orders, that information being relied on is current and complete. All products are sold subject to the terms and conditions of sale supplied at the time of order acknowledgment, including those pertaining to warranty, patent infringement, and limitation of liability.

TI warrants performance of its semiconductor products to the specifications applicable at the time of sale in accordance with TI's standard warranty. Testing and other quality control techniques are utilized to the extent TI deems necessary to support this warranty. Specific testing of all parameters of each device is not necessarily performed, except those mandated by government requirements.

Customers are responsible for their applications using TI components.

In order to minimize risks associated with the customer's applications, adequate design and operating safeguards must be provided by the customer to minimize inherent or procedural hazards.

TI assumes no liability for applications assistance or customer product design. TI does not warrant or represent that any license, either express or implied, is granted under any patent right, copyright, mask work right, or other intellectual property right of TI covering or relating to any combination, machine, or process in which such semiconductor products or services might be or are used. TI's publication of information regarding any third party's products or services does not constitute TI's approval, warranty or endorsement thereof.

Copyright © 2000, Texas Instruments Incorporated