



STD70NH02L

N-CHANNEL 24V - 0.0062 Ω - 60A IPAK/DPAK STripFET™ III POWER MOSFET

Table 1: General Features

TYPE	V _{DSS}	R _{DS(on)}	I _D
STD70NH02L	24 V	< 0.008 Ω	60 A(*)

- TYPICAL R_{DS(on)} = 0.0062 Ω @ 10 V
- TYPICAL R_{DS(on)} = 0.008 Ω @ 5 V
- R_{DS(ON)} * Q_g INDUSTRY'S BENCHMARK
- CONDUCTION LOSSES REDUCED
- SWITCHING LOSSES REDUCED
- LOW THRESHOLD DEVICE
- IN COMPLIANCE WITH THE 2002/95/EC EUROPEAN DIRECTIVE
- THROUGH-HOLE IPAK (TO-251) POWER PACKAGE IN TUBE (SUFFIX "-1")
- SURFACE-MOUNTING DPAK (TO-252) POWER PACKAGE IN TAPE & REEL (SUFFIX "T4")

DESCRIPTION

The STD70NH02L utilizes the latest advanced design rules of ST's proprietary STripFET™ technology. This is suitable for the most demanding DC-DC converter application where high efficiency is to be achieved.

APPLICATIONS

SPECIFICALLY DESIGNED AND OPTIMISED FOR HIGH EFFICIENCY DC/DC CONVERTERS

Figure 1: Package

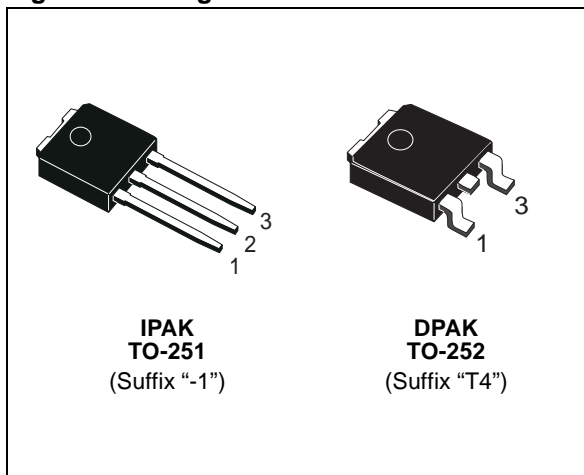


Figure 2: Internal Schematic Diagram

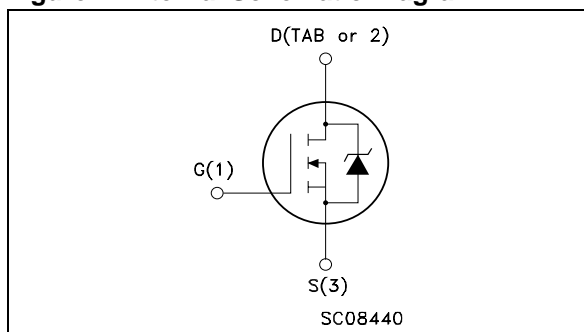


Table 2: Ordering Information

SALES TYPE	MARKING	PACKAGE	PACKAGING
STD70NH02LT4	D70NH02L	TO-252	TAPE & REEL
STD70NH02L-1	D70NH02L	TO-251	TUBE

Table 3: ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Value	Unit
V _{spike(1)}	Drain-source Voltage Rating	30	V
V _{DS}	Drain-source Voltage (V _{GS} = 0)	24	V
V _{DGR}	Drain-gate Voltage (R _{GS} = 20 k Ω)	24	V
V _{GS}	Gate- source Voltage	\pm 20	V
I _D (*)	Drain Current (continuous) at T _C = 25°C	60	A
I _D	Drain Current (continuous) at T _C = 100°C	50	A
I _{DM} (2)	Drain Current (pulsed)	240	A
P _{tot}	Total Dissipation at T _C = 25°C	70	W
	Derating Factor	0.47	W/°C
E _{AS} (3)	Single Pulse Avalanche Energy	360	mJ
T _{stg}	Storage Temperature	-55 to 175	°C
T _j	Max. Operating Junction Temperature		

Table 4: THERMAL DATA

Rthj-case	Thermal Resistance Junction-case	Max	2.14	°C/W
Rthj-amb	Thermal Resistance Junction-ambient	Max	100	°C/W
T _I	Maximum Lead Temperature For Soldering Purpose		275	°C

ELECTRICAL CHARACTERISTICS (T_{CASE} = 25 °C UNLESS OTHERWISE SPECIFIED)

Table 5: OFF

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
V _{(BR)DSS}	Drain-source Breakdown Voltage	I _D = 25 mA, V _{GS} = 0	24			V
I _{DSS}	Zero Gate Voltage Drain Current (V _{GS} = 0)	V _{DS} = 20 V V _{DS} = 20 V T _C = 125°C			1 10	μA μA
I _{GSS}	Gate-body Leakage Current (V _{DS} = 0)	V _{GS} = ± 20V			±100	nA

Table 6: ON (4)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
V _{GS(th)}	Gate Threshold Voltage	V _{DS} = V _{GS} I _D = 250 μA	1	1.8		V
R _{DS(on)}	Static Drain-source On Resistance	V _{GS} = 10 V I _D = 30 A V _{GS} = 5 V I _D = 15 A		0.0062 0.008	0.008 0.014	Ω Ω

Table 7: DYNAMIC

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
g _{fs} (4)	Forward Transconductance	V _{DS} = 10 V I _D = 18 A		27		S
C _{iss} C _{oss} C _{rss}	Input Capacitance Output Capacitance Reverse Transfer Capacitance	V _{DS} = 16V f = 1 MHz V _{GS} = 0		2050 545 70		pF pF pF
R _G	Gate Input Resistance	f = 1 MHz Gate DC Bias = 0 Test Signal Level = 20 mV Open Drain		1		Ω

ELECTRICAL CHARACTERISTICS (continued)

Table 8: SWITCHING ON

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
$t_{d(on)}$ t_r	Turn-on Delay Time Rise Time	$V_{DD} = 10\text{ V}$ $I_D = 30\text{ A}$ $R_G = 4.7\ \Omega$ $V_{GS} = 5\text{ V}$ (Resistive Load, Figure 17)		12 200		ns ns
Q_g Q_{gs} Q_{gd}	Total Gate Charge Gate-Source Charge Gate-Drain Charge	$V_{DD} = 10\text{ V}$ $I_D = 60\text{ A}$ $V_{GS} = 5\text{ V}$		17 7.7 3.5	22	nC nC nC
$Q_{oss}^{(5)}$	Output Charge	$V_{DS} = 10\text{ V}$ $V_{GS} = 0\text{ V}$		14		nC

Table 9: SWITCHING OFF

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
$t_{d(off)}$ t_f	Turn-off Delay Time Fall Time	$V_{DD} = 10\text{ V}$ $I_D = 30\text{ A}$ $R_G = 4.7\ \Omega$, $V_{GS} = 5\text{ V}$ (Resistive Load, Figure 17)		18 25	33	ns ns

Table 10: SOURCE DRAIN DIODE

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
I_{SD} I_{SDM}	Source-drain Current Source-drain Current (pulsed)				60 240	A A
$V_{SD}^{(4)}$	Forward On Voltage	$I_{SD} = 30\text{ A}$ $V_{GS} = 0$			1.3	V
t_{rr} Q_{rr} I_{RRM}	Reverse Recovery Time Reverse Recovery Charge Reverse Recovery Current	$I_{SD} = 60\text{ A}$ $di/dt = 100\text{ A}/\mu\text{s}$ $V_{DD} = 15\text{ V}$ $T_j = 150^\circ\text{C}$ (see test circuit, Figure 19)		36 65 3.6		ns nC A

(1) Guaranteed when external $R_g=4.7\ \Omega$ and $t_f < t_{fmax}$.
 (2) Pulse width limited by safe operating area
 (3) Starting $T_j = 25\ ^\circ\text{C}$, $I_D = 25\text{ A}$, $V_{DD} = 15\text{ V}$

(4) Pulsed: Pulse duration = 300 μs , duty cycle 1.5%.
 (5) $Q_{oss} = C_{oss} \cdot \Delta V_{in}$, $C_{oss} = C_{gd} + C_{ds}$. See Appendix A
 (*) Value limited by wire bonding

Figure 3: Safe Operating Area

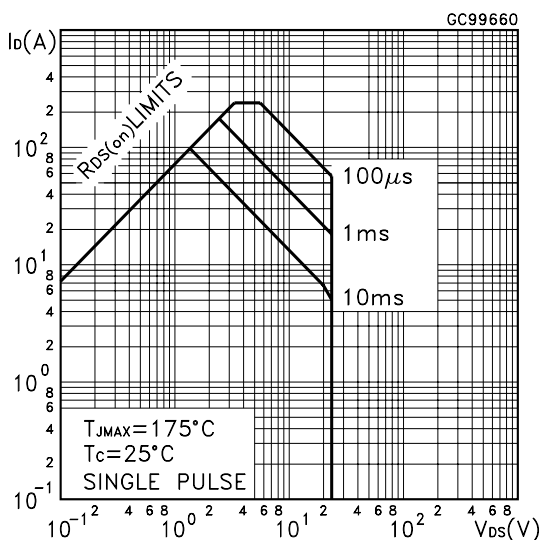


Figure 4: Thermal Impedance

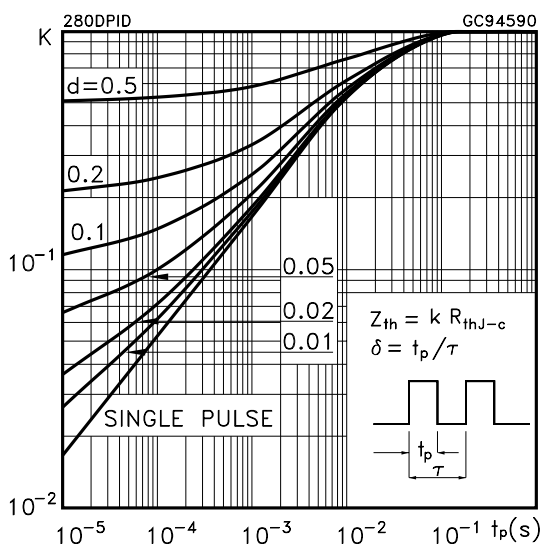


Figure 5: Output Characteristics

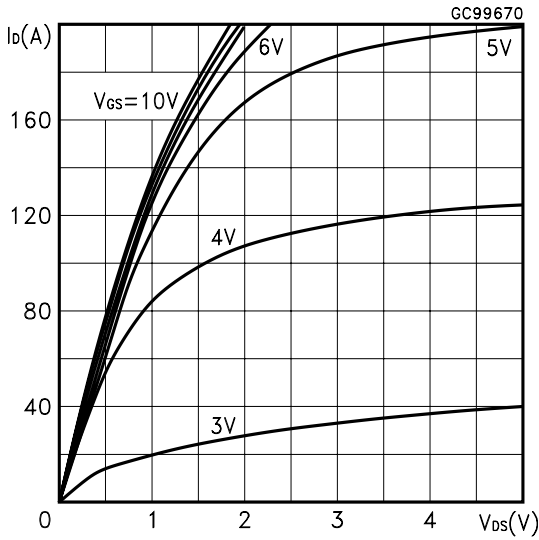


Figure 6: Transfer Characteristics

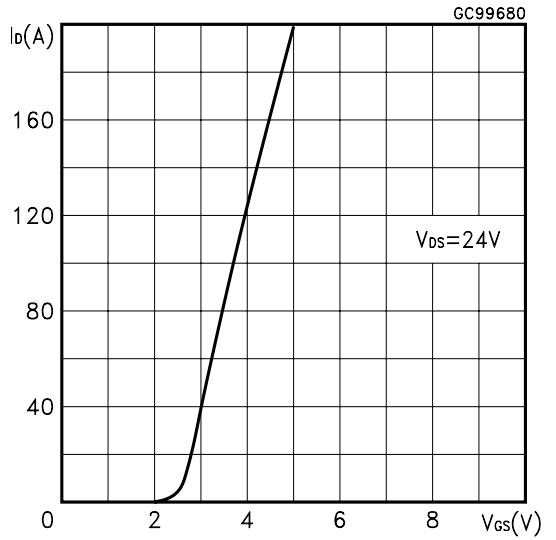


Figure 7: Transconductance

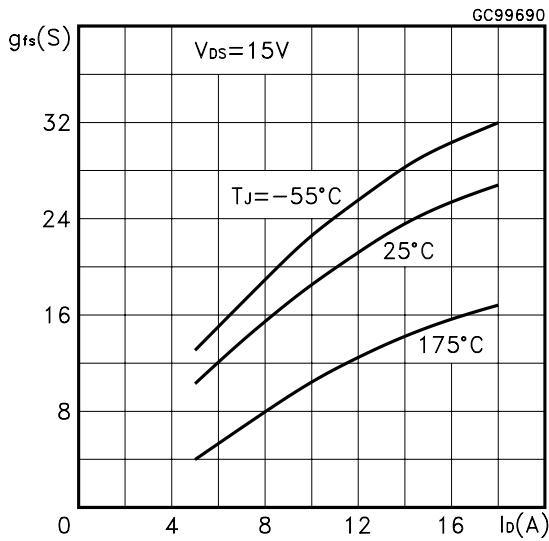


Figure 8: Static Drain-source On Resistance

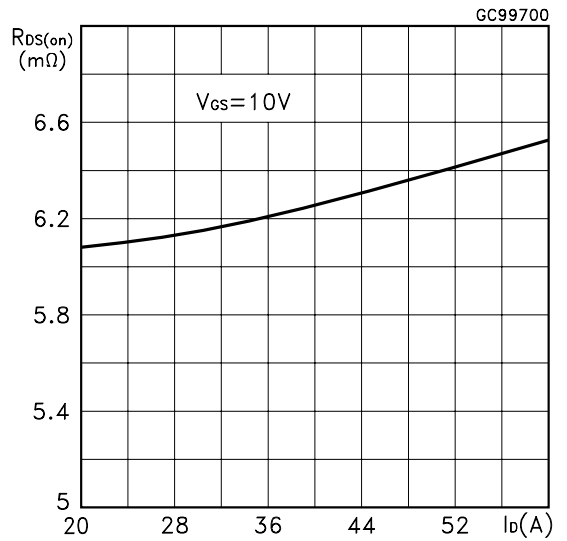


Figure 9: Gate Charge vs Gate-source Voltage

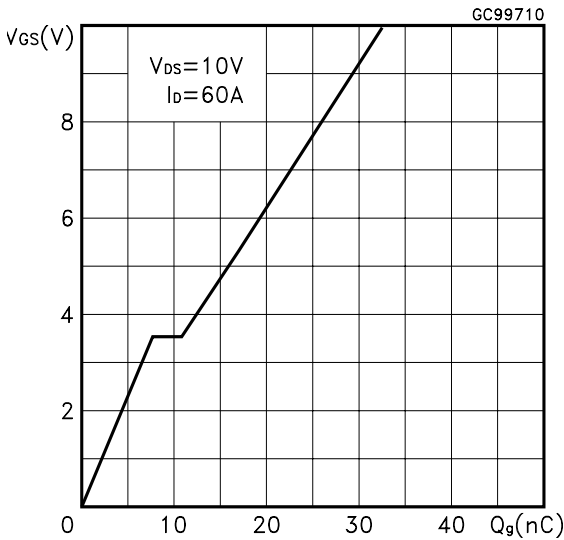


Figure 10: Capacitance Variations

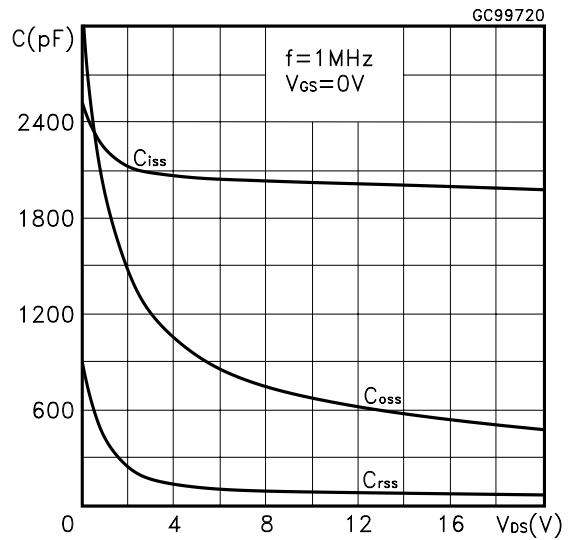


Figure 11: Normalized Gate Threshold Voltage vs Temperature

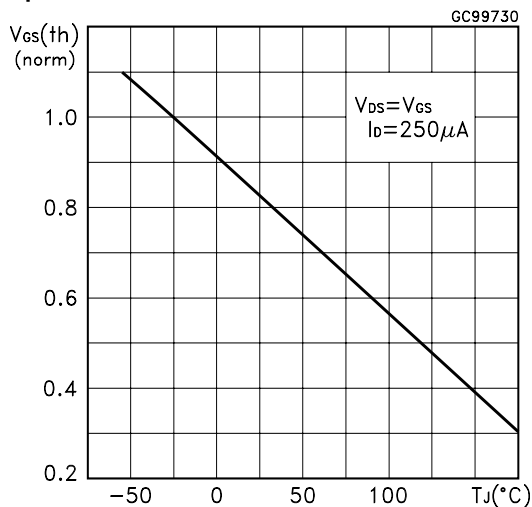


Figure 12: Normalized on Resistance vs Temperature

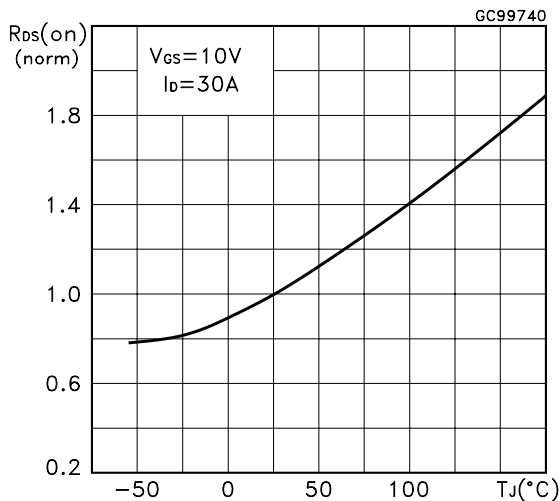


Figure 13: Source-drain Diode Forward Characteristics

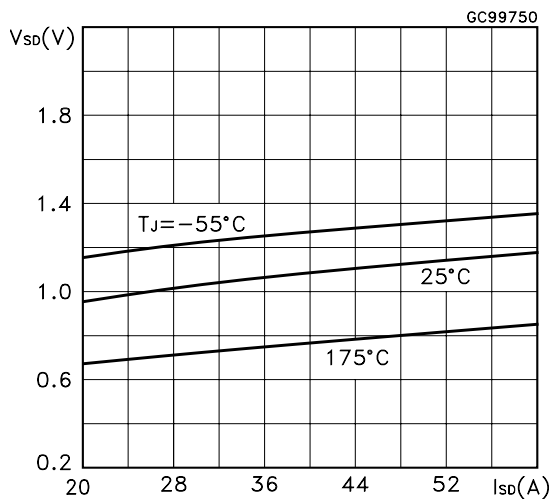


Figure 14: Normalized Breakdown Voltage vs Temperature

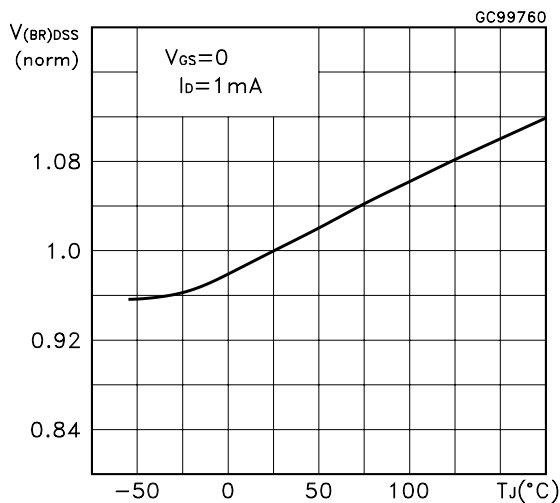


Figure 15: Unclamped Inductive Load Test Circuit

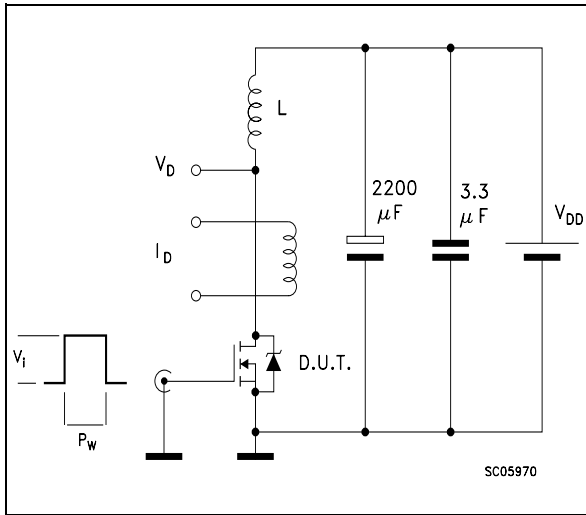


Figure 16: Unclamped Inductive Waveform

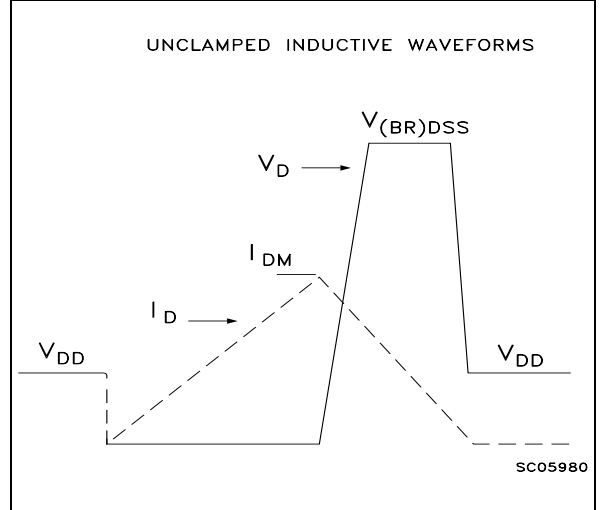


Figure 17: Switching Times Test Circuits For Resistive Load

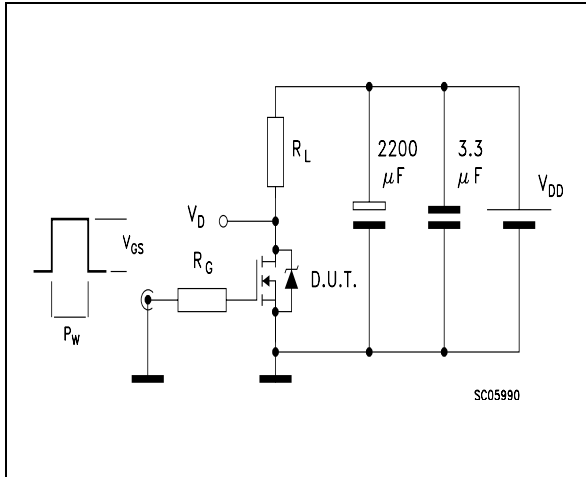


Figure 18: Gate Charge test Circuit

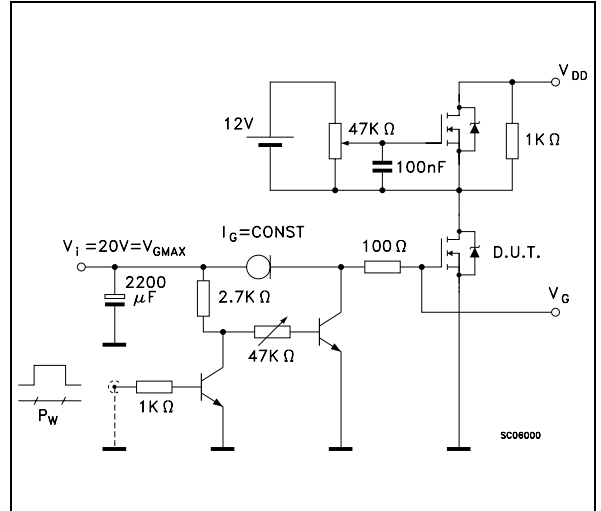
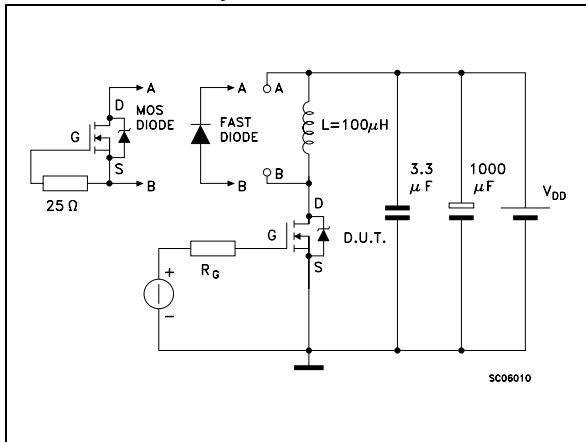
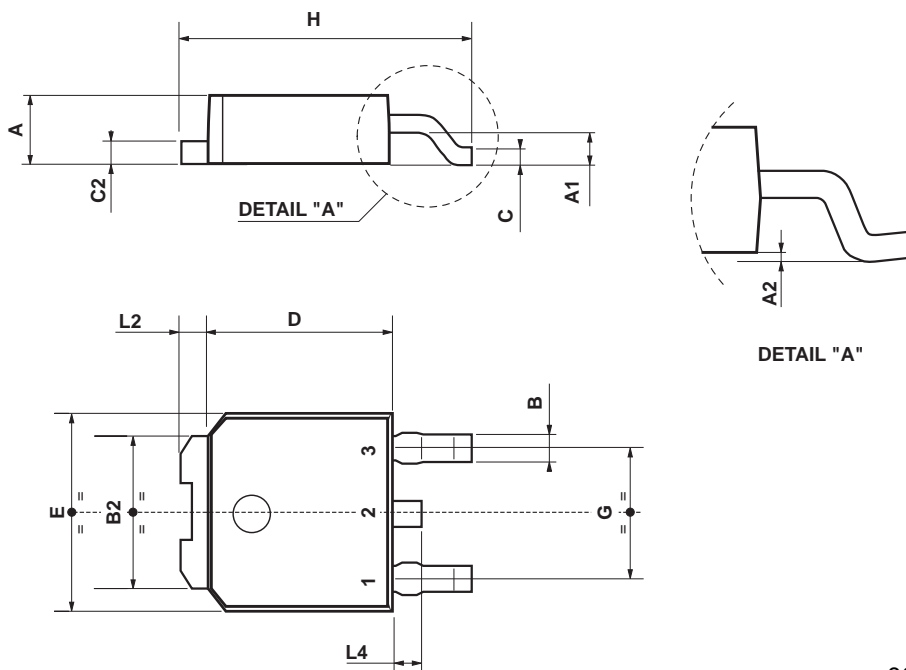


Figure 19: Test Circuit For Inductive Load Switching And Diode Recovery Times



TO-252 (DPAK) MECHANICAL DATA

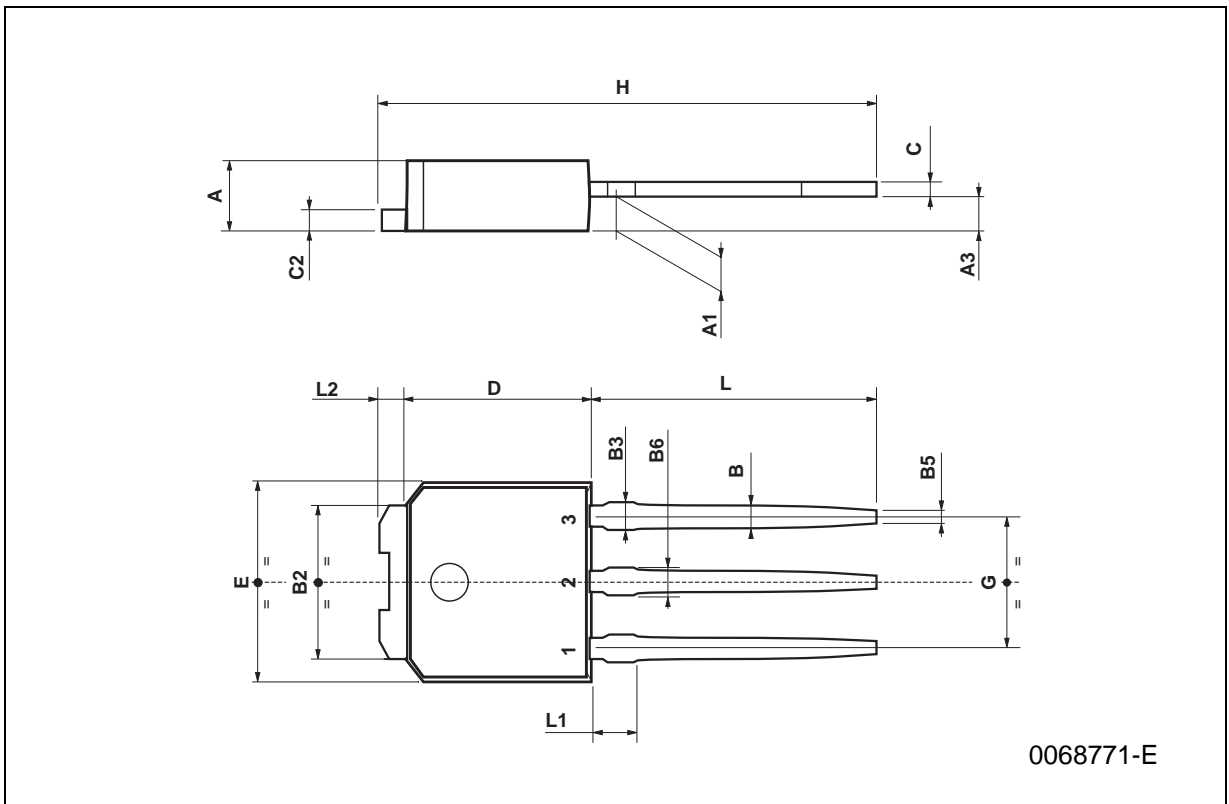
DIM.	mm			inch		
	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.
A	2.2		2.4	0.086		0.094
A1	0.9		1.1	0.035		0.043
A2	0.03		0.23	0.001		0.009
B	0.64		0.9	0.025		0.035
B2	5.2		5.4	0.204		0.212
C	0.45		0.6	0.017		0.023
C2	0.48		0.6	0.019		0.023
D	6		6.2	0.236		0.244
E	6.4		6.6	0.252		0.260
G	4.4		4.6	0.173		0.181
H	9.35		10.1	0.368		0.397
L2		0.8			0.031	
L4	0.6		1	0.023		0.039



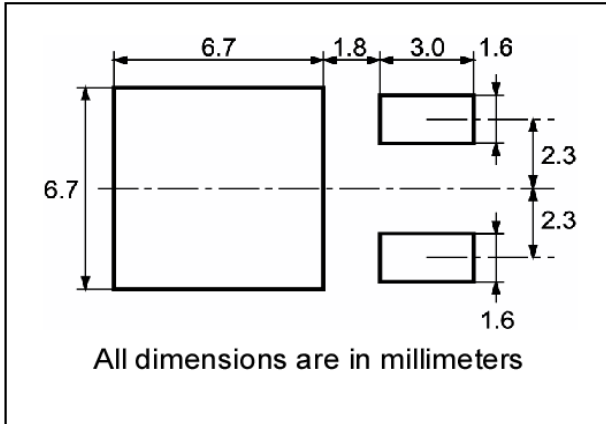
0068772-B

TO-251 (IPAK) MECHANICAL DATA

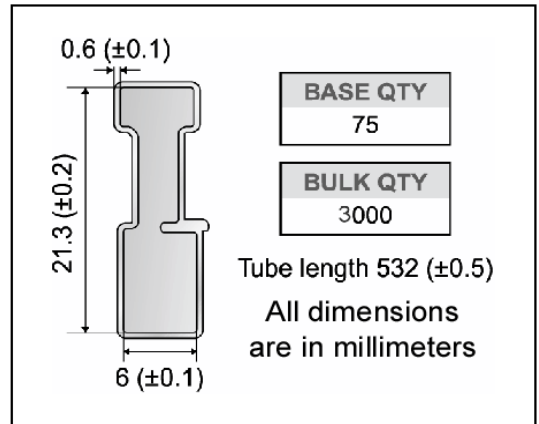
DIM.	mm			inch		
	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.
A	2.2		2.4	0.086		0.094
A1	0.9		1.1	0.035		0.043
A3	0.7		1.3	0.027		0.051
B	0.64		0.9	0.025		0.031
B2	5.2		5.4	0.204		0.212
B3			0.85			0.033
B5		0.3			0.012	
B6			0.95			0.037
C	0.45		0.6	0.017		0.023
C2	0.48		0.6	0.019		0.023
D	6		6.2	0.236		0.244
E	6.4		6.6	0.252		0.260
G	4.4		4.6	0.173		0.181
H	15.9		16.3	0.626		0.641
L	9		9.4	0.354		0.370
L1	0.8		1.2	0.031		0.047
L2		0.8	1		0.031	0.039



DPAK FOOTPRINT



TUBE SHIPMENT (no suffix)*



TAPE AND REEL SHIPMENT (suffix "T4")*

Diagram showing the tape mechanical data. The tape has a width of A and a thickness of T. The distance between the centerlines of the cavities is D. The distance from the centerline to the edge of the tape is B. The distance from the centerline to the edge of the hub is G. The distance from the centerline to the edge of the core is N. The distance from the centerline to the edge of the slot is C. The distance from the centerline to the edge of the top cover tape is K₀. The distance from the centerline to the edge of the bottom cover tape is B₀. The distance from the centerline to the edge of the top cover tape is B₁. The distance from the centerline to the edge of the bottom cover tape is D₁. The distance from the centerline to the edge of the top cover tape is E. The distance from the centerline to the edge of the bottom cover tape is F. The distance from the centerline to the edge of the top cover tape is P₁. The distance from the centerline to the edge of the bottom cover tape is P₂. The distance from the centerline to the edge of the top cover tape is P₀. The distance from the centerline to the edge of the bottom cover tape is W. The distance from the centerline to the edge of the top cover tape is R min. The distance from the centerline to the edge of the bottom cover tape is R min.

40 mm min. Access hole at slot location

Full radius

Tape slot in core for tape start 2.5mm min. width

G measured at hub

REEL MECHANICAL DATA

DIM.	mm		inch	
	MIN.	MAX.	MIN.	MAX.
A		330		12.992
B	1.5		0.059	
C	12.8	13.2	0.504	0.520
D	20.2		0.795	
G	16.4	18.4	0.645	0.724
N	50		1.968	
T		22.4		0.881

BASE QTY	BULK QTY
1000	1000

TAPE MECHANICAL DATA

DIM.	mm		inch	
	MIN.	MAX.	MIN.	MAX.
A0	6.8	7	0.267	0.275
B0	10.4	10.6	0.409	0.417
B1		12.1		0.476
D	1.5	1.6	0.059	0.063
D1	1.5		0.059	
E	1.65	1.85	0.065	0.073
F	7.4	7.6	0.291	0.299
K0	2.55	2.75	0.100	0.108
P0	3.9	4.1	0.153	0.161
P1	7.9	8.1	0.311	0.319
P2	1.9	2.1	0.075	0.082
R	40		1.574	
W	15.7	16.3	0.618	0.641

Diagram showing the tape mechanical data. The tape has a width of W and a thickness of T. The distance between the centerlines of the cavities is D. The distance from the centerline to the edge of the tape is B. The distance from the centerline to the edge of the hub is G. The distance from the centerline to the edge of the core is N. The distance from the centerline to the edge of the slot is C. The distance from the centerline to the edge of the top cover tape is K₀. The distance from the centerline to the edge of the bottom cover tape is B₀. The distance from the centerline to the edge of the top cover tape is B₁. The distance from the centerline to the edge of the bottom cover tape is D₁. The distance from the centerline to the edge of the top cover tape is E. The distance from the centerline to the edge of the bottom cover tape is F. The distance from the centerline to the edge of the top cover tape is P₁. The distance from the centerline to the edge of the bottom cover tape is P₂. The distance from the centerline to the edge of the top cover tape is P₀. The distance from the centerline to the edge of the bottom cover tape is W. The distance from the centerline to the edge of the top cover tape is R min. The distance from the centerline to the edge of the bottom cover tape is R min.

For machine ref. only including draft and radii concentric around B₀

10 pitches cumulative tolerance on tape + / - 0.2 mm

Center line of cavity

User Direction of Feed

FEED DIRECTION

Bending radius

*on sales type

Table 11:Revision History

Date	Revision	Description of Changes
March 2005	4.0	ADDED PACKAGE IPAK

Information furnished is believed to be accurate and reliable. However, STMicroelectronics assumes no responsibility for the consequences of use of such information nor for any infringement of patents or other rights of third parties which may result from its use. No license is granted by implication or otherwise under any patent or patent rights of STMicroelectronics. Specifications mentioned in this publication are subject to change without notice. This publication supersedes and replaces all information previously supplied. STMicroelectronics products are not authorized for use as critical components in life support devices or systems without express written approval of STMicroelectronics.

The ST logo is registered trademark of STMicroelectronics
All other names are the property of their respective owners.

© 2005 STMicroelectronics - All Rights Reserved

STMicroelectronics group of companies

Australia - Belgium - Brazil - Canada - China - Czech Republic - Finland - France - Germany - Hong Kong - India - Israel - Italy - Japan -
Malaysia - Malta - Morocco - Singapore - Spain - Sweden - Switzerland - United Kingdom - United States of America.

www.st.com