



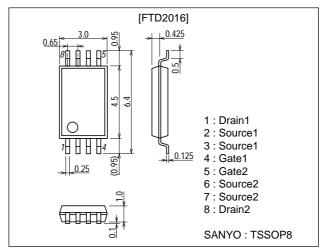
Preliminary

Features

- · Low ON-resistance.
- 2.5V drive.
- Mounting height 1.1mm.
- Composite type, facilitating high-density mounting.

Package Dimensions

unit : mm 2155A



Specifications

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	VDSS		30	٧
Gate-to-Source Voltage	VGSS		±10	٧
Drain Current (DC)	ID		4	Α
Drain Current (Pulse)	IDP	PW≤10μs, duty cycle≤1%	20	Α
Allowable Power Dissipation	PD	Mounted on a ceramic board (1000mm ² X0.8mm) 1unit	0.8	W
Total Dissipation	PT	Mounted on a ceramic board (1000mm ² X0.8mm)	1.3	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		-55 to +150	°C

Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	OTHE
Drain-to-Source Breakdown Voltage	V(BR)DSS	ID=1mA, VGS=0	30			V
Zero-Gate Voltage Drain Current	IDSS	V _{DS} =30V, V _{GS} =0			1	μΑ
Gate-to-Source Leakage Current	IGSS	V _{GS} =±8V, V _{DS} =0			±10	μΑ
Cutoff Voltage	VGS(off)	VDS=10V, ID=1mA	0.4		1.3	V
Forward Transfer Admittance	yfs	V _{DS} =10V, I _D =4A	7.7	11		S

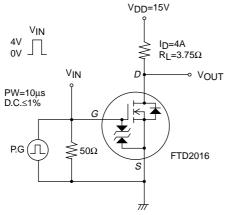
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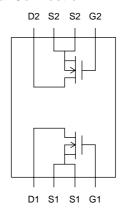
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Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Offic
Static Drain-to-Source On-State Resistance	RDS(on)	ID=4A, VGS=4V		37	49	mΩ
		I _D =2A, V _G S=2.5V		47	66	mΩ
Input Capacitance	Ciss	V _{DS} =10V, f=1MHz		560		pF
Output Capacitance	Coss	V _{DS} =10V, f=1MHz		120		pF
Reverse Transfer Capacitance	Crss	V _{DS} =10V, f=1MHz		70		pF
Turn-ON Delay Time	t _d (on)	See specified Test Circuit		12		ns
Rise Time	tr	See specified Test Circuit		85		ns
Turn-OFF Delay Time	t _d (off)	See specified Test Circuit		55		ns
Fall Time	tf	See specified Test Circuit		90		ns
Total Gate Charge	Qg	V _{DS} =10V, V _{GS} =10V, I _D =4A		24		nC
Gate-to-Source Charge	Qgs	V _{DS} =10V, V _{GS} =10V, I _D =4A		1.5		nC
Gate-to-Drain "Miller" Charge	Qgd	V _{DS} =10V, V _{GS} =10V, I _D =4A		3.2		nC
Diode Forward Voltage	V _{SD}	I _S =4A, V _{GS} =0			1.2	V

Switching Time Test Circuit



Electrical Connection



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