5V/3.3V DIFFERENTIAL RECEIVER

SY10EL16V SY100EL16V FINAL

FEATURES

- 3.3V and 5V power supply options
- 250ps propagation delay
- **■** High bandwidth output transitions
- Internal 75K Ω input pull-down resistors
- Replaces SY10/100EL16
- **■** Improved output waveform characteristics
- Available in 8-pin (3mm) MSOP and SOIC package

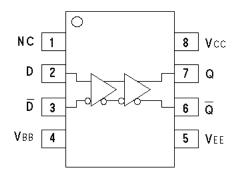
DESCRIPTION

The SY10/100EL16V are differential receivers. The devices are functionally equivalent to the E116 devices, with higher performance capabilities. With output transition times significantly faster than the E116, the EL16V is ideally suited for interfacing with high-frequency sources.

The EL16V provides a VBB output for either single-ended use or as a DC bias for AC coupling to the device. The VBB pin should be used only as a bias for the EL16V as its current sink/source capability is limited. Whenever used, the VBB pin should be bypassed to ground via a $0.01\mu f$ capacitor.

Under open input conditions (pulled to VEE), internal input clamps will force the Q output LOW.

PIN CONFIGURATION/BLOCK DIAGRAM



MSOP and SOIC

PIN NAMES

Pin	Function
D	Data Inputs
Q	Data Outputs
Vвв	Reference Voltage Output

DC ELECTRICAL CHARACTERISTICS(Note 1, 2)

VEE = VEE (Min.) to VEE (Max.); VCC = GND

		Ta = -40°C		-	TA = 0°C T		Ta = +25°C		Ta = +85°C					
Symbol	Parameter	Min.	Тур.	Max.	Min.	Тур.	Max.	Min.	Тур.	Max.	Min.	Тур.	Max.	Unit
IEE	Power Supply Current													mA
	10EL	_	18	22	9	18	22	9	18	22	9	18	22	
	100EL	_	18	22	9	18	22	9	18	22	9	21	26	
VBB	Output Reference													V
	Voltage 10EL	-1.43	_	-1.30	-1.38	_	-1.27	-1.35	_	-1.25	-1.31	l —	-1.19	
	100EL	-1.38	_	-1.26	-1.38	_	-1.26	-1.38	_	-1.26	-1.38	_	-1.26	
Iн	Input HIGH Current	_	_	150	_	_	150	_	_	150	_	_	150	μΑ

Note 1. Parametric values specified at:

10/100EL16V Series:

-3.0V to -5.5V.

Note 2. specification for packaged product only.

AC ELECTRICAL CHARACTERISTICS(Note 1, 5)

VEE = VEE (Min.) to VEE (Max.); VCC = GND

		Ta = -40°C		Ta = 0°C			TA = +25°C			TA = +85°C				
Symbol	Parameter	Min.	Тур.	Max.	Unit									
tPLH tPHL	Propagation Delay to Output D (Diff) D (SE)	125 75	250 250	375 425	175 125	250 250	325 375	175 125	250 250	325 375	205 155	280 280	355 405	ps
tskew	Duty Cycle Skew ⁽²⁾ (Diff)	_	5	_	_	5	20	_	5	20		5	20	ps
VPP	Minimum Input Swing(3)	150	_	_	150	_	_	150	_	_	150	_	_	mV
VCMR	Common Mode Range ⁽⁴⁾	-1.3	_	-0.4	-1.4	_	-0.4	-1.4	_	-0.4	-1.4	_	-0.4	V
tr tf	Output Rise/Fall Times Q (20% to 80%)	100	225	350	100	225	350	100	225	350	100	225	350	ps

Note 1. Parametric values specified at:

10/100EL16V Series:

-3.0V to -5.5V.

- Note 2. Duty cycle skew is the difference between a tPLH and tPHL propagation delay through a device.
- Note 3. Minimum input swing for which AC parameters are guaranteed. The device has a DC gain of ≈40.
- Note 4. The CMR range is referenced to the most positive side of the differential input signal. Normal operation is obtained if the HIGH level falls within the specified range and the peak-to-peak voltage lies between VPP min. and 1V. The lower end of the CMR range varies 1:1 with VEE. The numbers in the spec table assume a nominal VEE = -3.3V. Note for PECL operation, the VCMR (min) will be fixed at 3.3V |VCMR (min)|.
- Note 5. Specification for packaged product only.

PRODUCT ORDERING CODE

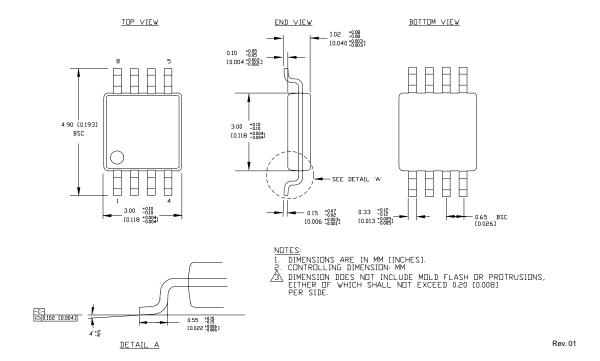
Ordering Code	Package Type	Operating Range	Marking Code		
SY10EL16VKCTR*	K8-1	Commercial	HL16V		
SY100EL16VKCTR*	K8-1	Commercial	XL16V		
SY10EL16VZC	Z8-1	Commercial	HEL16V		
SY10EL16VZCTR*	Z8-1	Commercial	HEL16V		
SY100EL16VZC	Z8-1	Commercial	XEL16V		
SY100EL16VZCTR*	Z8-1	Commercial	XEL16V		

Ordering Code	Package Type	Operating Range	Marking Code		
SY10EL16VKITR*(1)	K8-1	Industrial	HL16V		
SY100EL16VKITR*(1)	K8-1	Industrial	XL16V		
SY10EL16VZI ⁽¹⁾	Z8-1	Industrial	HEL16V		
SY10EL16VZITR*(1)	Z8-1	Industrial	HEL16V		
SY100EL16VZI ⁽¹⁾	Z8-1	Industrial	XEL16V		
SY100EL16VZITR*(1)	Z8-1	Industrial	XEL16V		

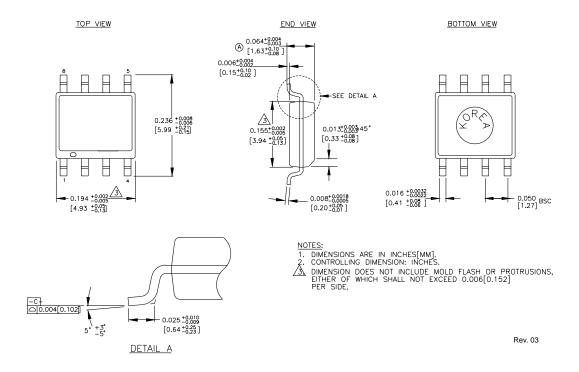
Note 1. Recommended for new designs.

^{*}Tape and Reel

8 LEAD MSOP (K8-1)



8 LEAD SOIC(Z8-1)



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