

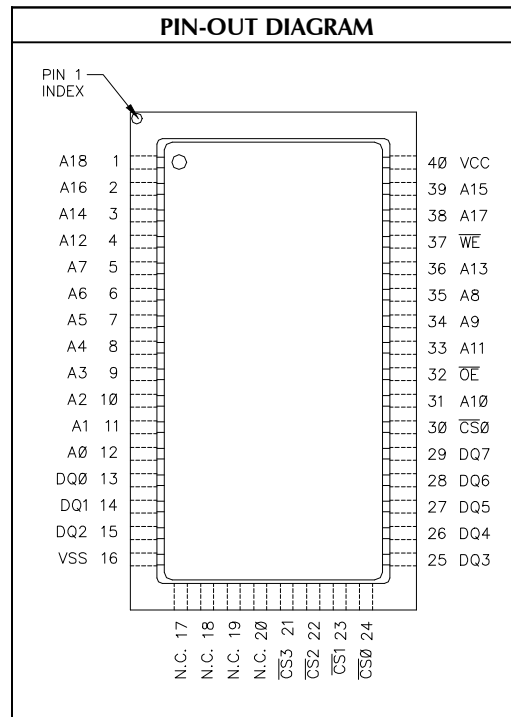
DESCRIPTION:

The LP-Stack™ series is a family of interchangeable memory modules. The 4 Megabit SRAM is a member of this family which utilizes the new and innovative space saving TSOP technology. The modules are constructed with 512K x 8, 5 Volt SRAM's.

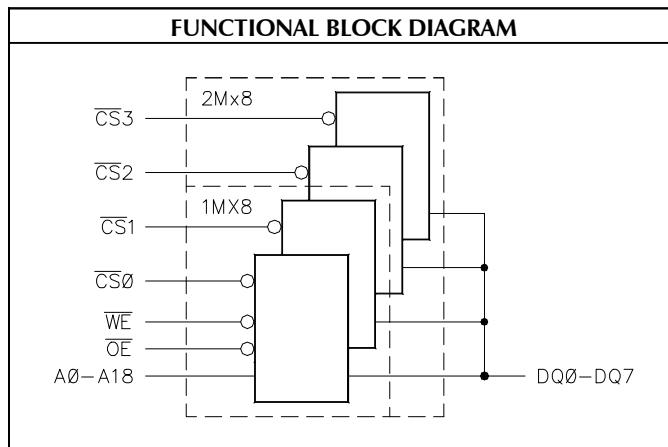
The 4 Megabit based LP-Stack™ modules have been designed to fit in the same footprint as the 512K x 8 SRAM TSOP monolithic. This allows the memory board designer to upgrade the memory in their products without redesigning the memory board, thus saving time and money.

FEATURES:

- Configurations Available:
 - 8 Megabit: 1 Meg x 8
 - 16 Megabit: 2 Meg x 8
- Access Times: 55, 70, 85ns (max.)
- Fully Static Operation - No Clock or Refresh Required
- Equal Access and Cycle Times
- 5.0 Volt Supply
- TTL Compatible Inputs and Outputs
- Package: 40 Pin Leadless LP-Stack™
 - DPS1MX8MY5 (2-High LP-Stack)
 - DPS2MX8MY5 (4 High LP-Stack)



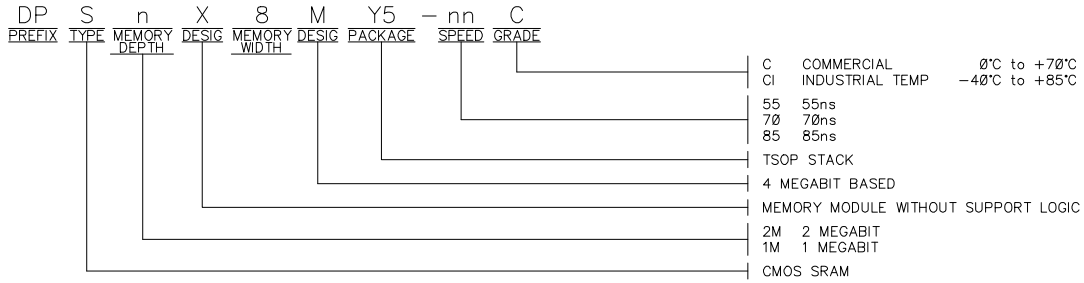
PIN NAMES	
A0 - A18	Address
DQ0 - DQ7	Data In / Data Out
CS0 - CS7	Chip Selects
WE	Write Enable
OE	Output Enable
VDD	Power Supply (+5V)
VSS	Ground
N.C.	No Connect



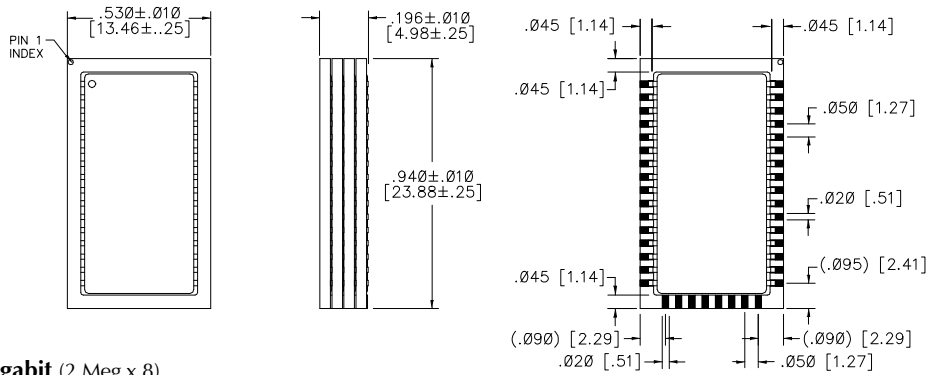
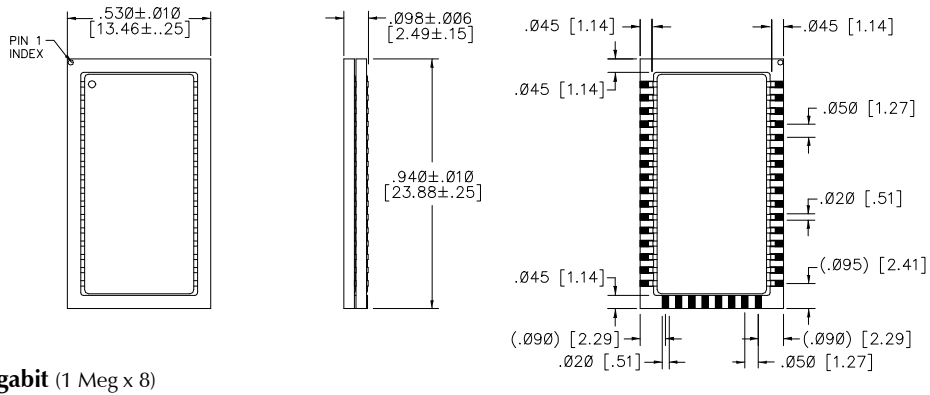
4 Megabit SRAM Based

Dense-Pac Microsystems, Inc.

ORDERING INFORMATION



MECHANICAL DRAWING



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